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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/989,817	11/19/2001	Matthew A. Pleis	CYPR-CD01191M	1792

7590 01/12/2005

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EXAMINER

ELAMIN, ABDELMONIEM I

ART UNIT	PAPER NUMBER
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2116

DATE MAILED: 01/12/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/989,817

Applicant(s)

PLEIS ET AL.

Examiner

A Elamin

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 08 October 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1,2,4-10,12-18,20 and 21 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 17-18, 20-21 is/are allowed.
- 6) ☒ Claim(s) 1,2,4,5,10,12 and 13 is/are rejected.
- 7) ☒ Claim(s) 6-9 and 14-16 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims *1, 2, 4-5, 10 and 12-13* are rejected under 35 U.S.C. 102(e) as being anticipated by Insenser Farre et al (“Insenser” hereafter), US. Pat. No. 6,460,172.
3. Claims *1, 10*, Insenser teaches a circuit [*see Fig. 1*], comprising: a bus for communicating data [*element 9 of Fig. 1*]; a microprocessor for processing data, said microprocessor coupled to said bus [*not explicitly shown in the figure, but taught in col. 2, lines 40-51, col. 3, lines 2-24 as on-chip microprocessor*]; a programmable functional component coupled to said bus, wherein said programmable functional component includes a plurality of functional blocks programmable to provide of functions and configurations [*see Fig. 1, col. 1, lines 15-19, col. 2, lines 1-14*]; a memory for storing data including information associated with said functions and configurations, said memory coupled to said bus [*element 1 of fig. 1*]; and a programmably configurable external communication port for communicatively coupling with external devices relative to said circuit [*element 5 Fig. 1, col. 3, lines 18-19, see also col. 2, lines 1-14 for background teaching of programmable logic blocks*].

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4. Claims 2, Insenser teaches said programmable functional component includes: a programmable interconnect for coupling components to said bus [*see connection to 3 and 4*]; an analog functional block configurable to perform analog functions, said analog functional block coupled to said interconnect [*element 4 of Fig. 1*]; and a digital functional block configurable to perform digital functions, said digital functional block coupled to said interconnect [*see element 3 of Fig. 1*].

5. Claim 4, Insenser teaches said memory stores a plurality of configuration images that define the configuration and functionality of said circuit [*col. 2, lines 14-21*].

6. Claims 5 and 10, Insenser teaches said circuit is automatically reconfigured by activating different ones of said plurality of configuration images based upon the existence of a predetermined condition or event [*col. 1, line 63 thru col. 2, line 13*].

7. Claim 12, Insenser teaches said electronic device is a programmable integrated circuit [*col. 1, lines 63-64*].

8. Claim 13, Insenser teaches said first configuration image and said second configuration image define different functions and configurations for components of said electronic device [*col. 2, lines 14-21*].

Allowable Subject Matter

9. Claims 6-9 and 14-16 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
10. Claims 17-18 and 20-21 are allowed.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to A Elamin whose telephone number is (571) 272-3674. The examiner can normally be reached on MON-FRI 9:30 AM - 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne Browne can be reached on (571) 272-3670. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A Elamin
Primary Examiner
Art Unit 2116

January 8, 2005

A. ELAMIN
PRIMARY EXAMINER

